

DFM 36MX296-ML

Technical Reference Manual



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1 Quick Facts

General	
Dynamic Range	10 bit
Resolution	1440x1080
Frame Rate at Full Resolution	60
Pixel Formats	10-Bit Bayer (RG)
Optical Interface	
Sensor Type	Sony IMX296LQR-C
Shutter Type	Global
Sensor Format	1/2.9 inch
Pixel Size	3.45 μm
Electrical Interface	
Interface	The Imaging Source MIPI CSI-2 Sensor Board Connector
Number of active CSI lanes	1
Supply voltage	5V ($\pm 10\%$)
Current consumption	approx 185 mA @ 5 VDC
Mechanical Data	
Dimensions	H: 30 mm, W: 30 mm, L: 5.45 mm
Mass	4 g
Adjustments	
Shutter	1 μs to 1 s
Gain	0 dB to 48 dB

Environmental	
Temperature (operating)	-5 °C to 45 °C
Temperature (storage)	-20 °C to 60 °C
Humidity (operating)	20 % to 80 % (non-condensing)
Humidity (storage)	20 % to 95 % (non-condensing)

*) See section Temperature Measurement Point for details.

2 Electrical Characteristics

2.1 Absolute Maximum Ratings

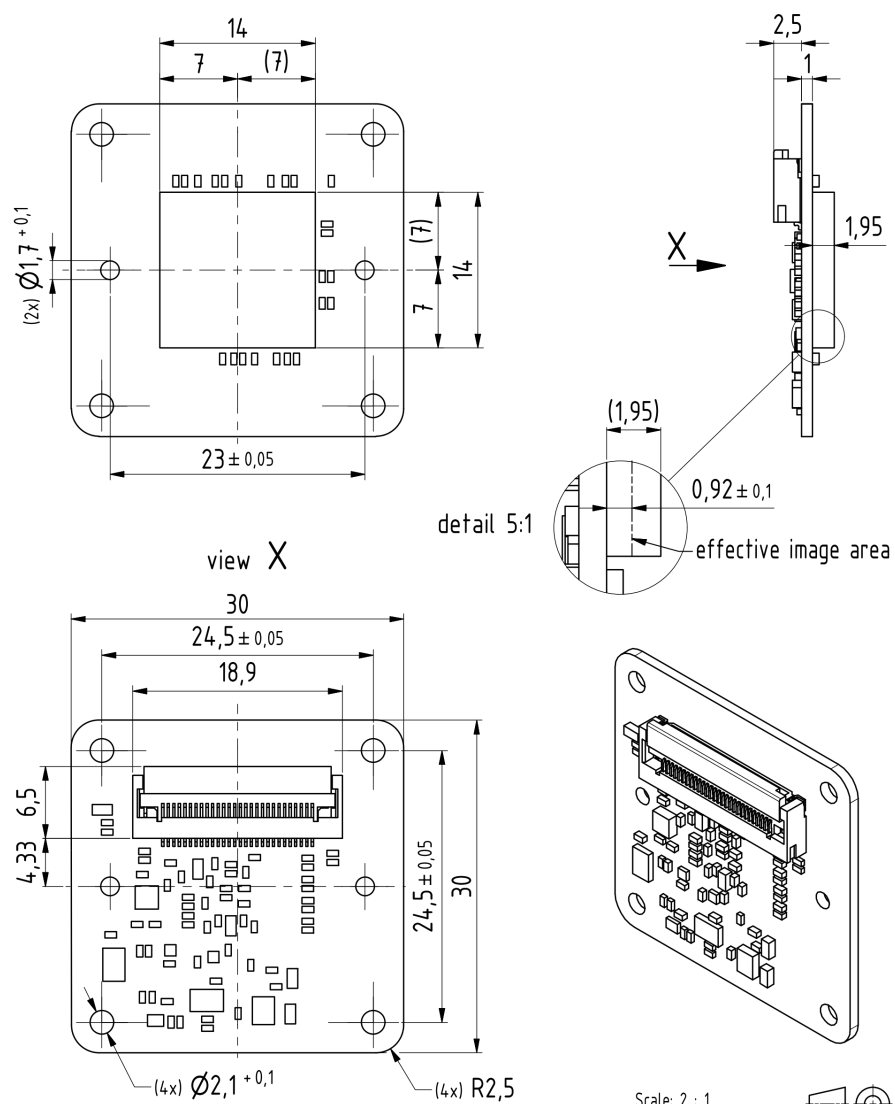
Item	Symbol	Pins	Min	Max	Unit
Supply voltage	V _{IN}	+5V_VDD	-0.3	+6.0	V
I/O voltage	V _{IO}	CAM_PWR RESET CLK STROBE TRIGGER	-0.3	+2.1	V
I2C voltage	V _{I2C}	I2C_SCL I2C_SDA	-0.3	+2.1	V

2.2 Recommended Operating Conditions

Item	Symbol	Pins	Min	Typ	Max	Unit
Supply voltage	V _{IN}	+5V_VDD	4.5	5.0	5.5	V
I/O voltage	V _{IO}	CAM_PWR RESET CLK STROBE TRIGGER	1.7	1.8	1.9	V
I2C voltage	V _{I2C}	I2C_SCL I2C_SDA	1.7	1.8	1.9	V

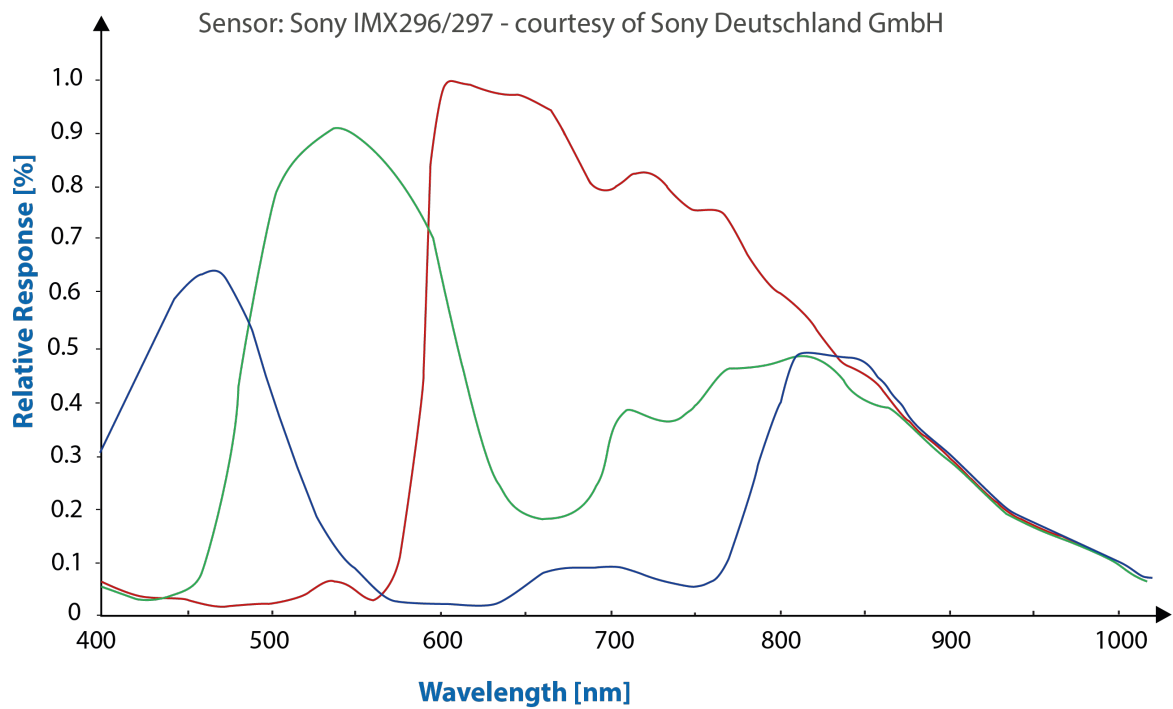
3 Dimensional Diagrams

3.1 DFM 36MX296-ML Board Camera



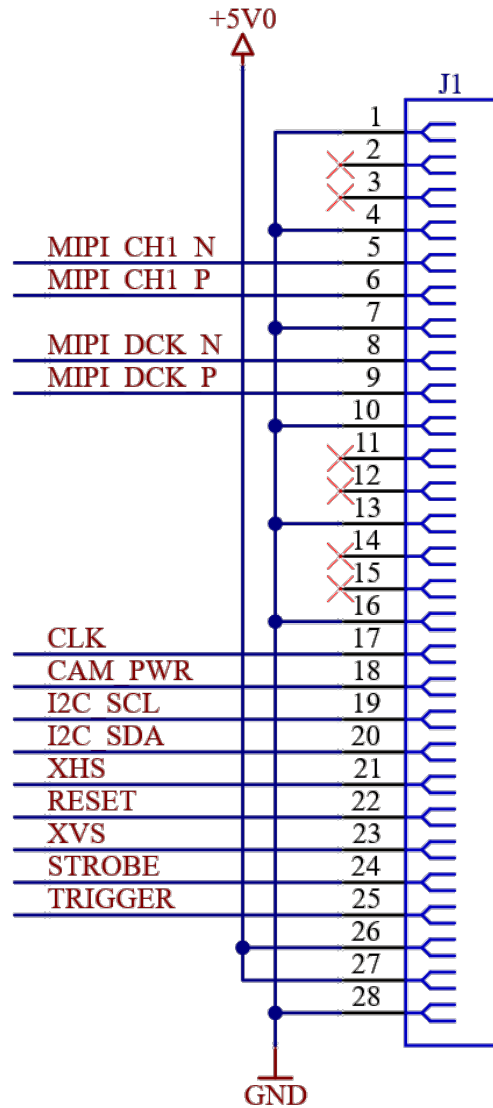
4 Spectral Characteristics

4.1 Spectral Sensitivity - IMX296LQR-C



5 Connector Description

The DFM 36MX296-ML sensor board is connected to the system via the *The Imaging Source MIPI CSI-2 Sensor Board Connector*.





#	Name	Type	Description
1	GND	GND	Ground
2	-	NC	
3	-	NC	
4	GND	GND	Ground
5	MIPI_CH1_N	O	MIPI CSI-2 output
6	MIPI_CH1_P	O	MIPI CSI-2 output
7	GND	GND	Ground
8	MIPI_DCK_N	O	MIPI CSI-2 clock
9	MIPI_DCK_P	O	MIPI CSI-2 clock
10	GND	GND	Ground
11	-	NC	
12	-	NC	
13	GND	GND	Ground
14	-	NC	
15	-	NC	
16	GND	GND	Ground
17	CLK	I	Reference clock input (with 1k pull-down/termination resistor on sensor board)
18	CAM_PWR	I	High active camera power enable signal (10k pull-down on sensor board)
19	I2C_SCL	I/O	I2C serial clock
20	I2C_SDA	I/O	I2C serial data
21	RESERVED_1	I	Do not use
22	RESET	I	Reset sensor to default state when low (2.2k pull-down on sensor board)
23	RESERVED_2	I	Do not use
24	STROBE	O	Strobe output
25	TRIGGER	I	Trigger input (weak pulldown on sensor board)
26	+5V_VDD	PWR	5V ($\pm 10\%$) power supply
27	+5V_VDD	PWR	5V ($\pm 10\%$) power supply
28	GND	GND	Ground



All I/Os have the same I/O voltage of 1.8V. The manufacturer part number of the Hirose connector is FH28D-28S-0.5SH(98).



6 CSI Lane Configurations

The following table shows the relationship between used CSI lanes and maximum frame rate:

No of CSI Lanes	Bits Per Pixel	Maximum Frame Rate at Full Resolution
1	10	120

7 I2C Devices

There are multiple I2C devices on the DFM 36MX296-ML sensor board. The following table describes the parts and their I2C addresses:

Address (7-bit)	Device	Description
0x1A	IMX296LQR-C	Image Sensor
0x40 (*)	LCMXO3L-1300E	Trigger Control FPGA (configuration)
0x42 (*)	LCMXO3L-1300E	Trigger Control FPGA (control)
0x50	AT24C256C	EEPROM
0x57	AT24C02C	EEPROM

(*) Only present on sensor board revision 2.00 or later.



8 Programming the Image Sensor

The data sheet for the IMX296LQR-C image sensor is not publicly available.

8.1 Input Clock

The CLK pin has to be connected to a clock source. The following table lists the ranges of clock frequencies that are supported by the image sensor:

Minimum	Typical	Maximum	Unit
35.64	37.125	37.867	MHz
51.84	54	55.08	MHz
71.28	74.25	75.735	MHz

The driver provided by The Imaging Source assumes a CLK frequency of **37 MHz**. For quick integration with existing software, using this frequency is recommended.

8.2 Power-up Sequence

Delay	Action
-	Set RESET to Hi-Z
-	Set CAM_PWR to Hi-Z
-	Supply 5V to 5V_VDD
-	Supply sensor clock to CLK
1 μ s	Set CAM_PWR to high
20 μ s	Set RESET to high
11 ms	Write sensor registers

8.3 Further Assistance

For more detailed information, register settings and assistance integrating the sensor board into your product, please contact The Imaging Source support.



9 Trigger Control FPGA

In order to handle complex trigger/strobe functions of the image sensor, a FPGA is present on sensor board revision 2.00 and above.

A reference driver implementation is available upon request.



DFM 36MX296-ML

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All weights and dimensions are approximate. Unless otherwise specified, the lenses shown in the context of cameras are not shipped with these cameras.

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